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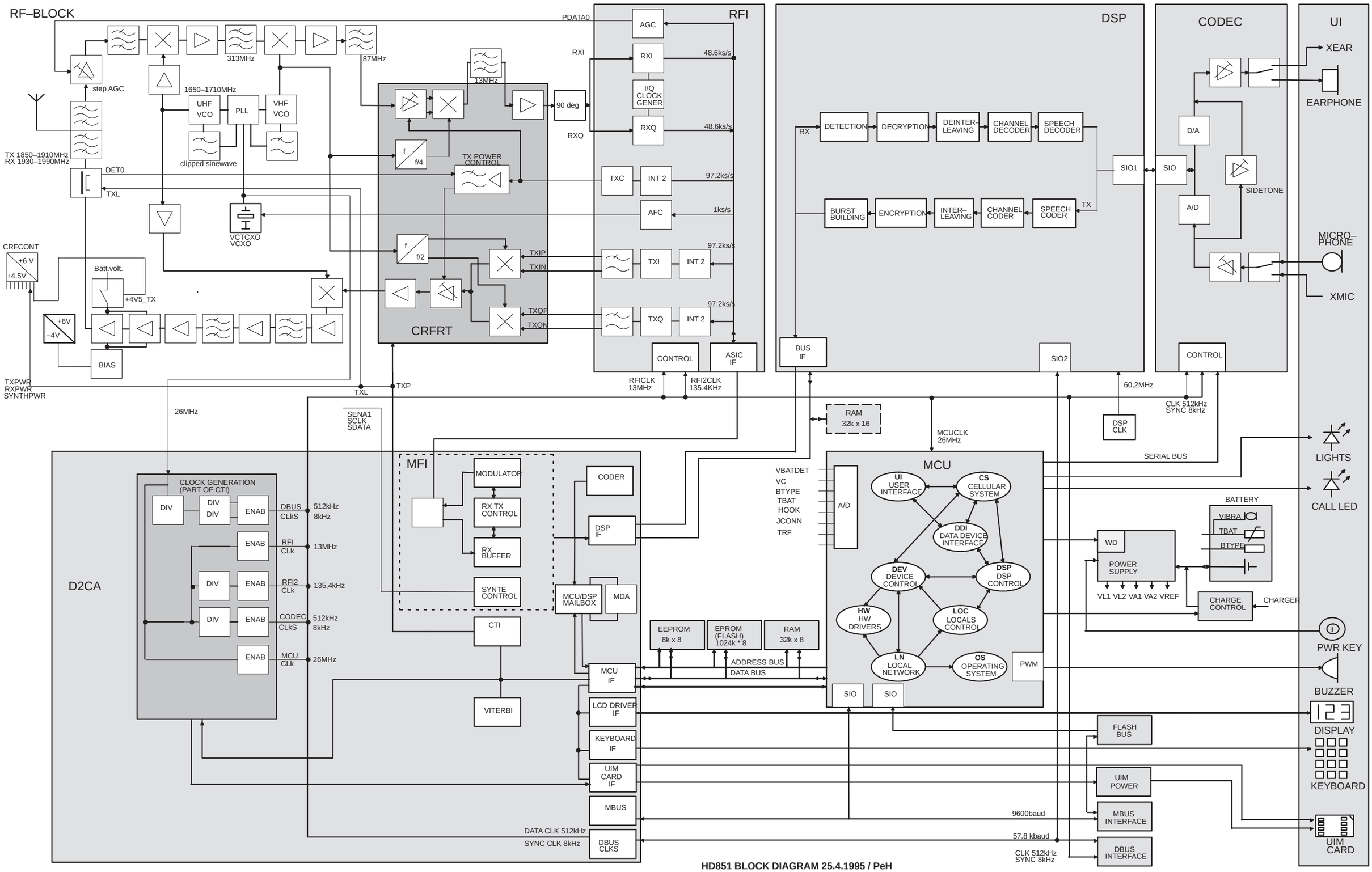
Block Diagram



HD851 BLOCK DIAGRAM 25.4.1995 / PeH

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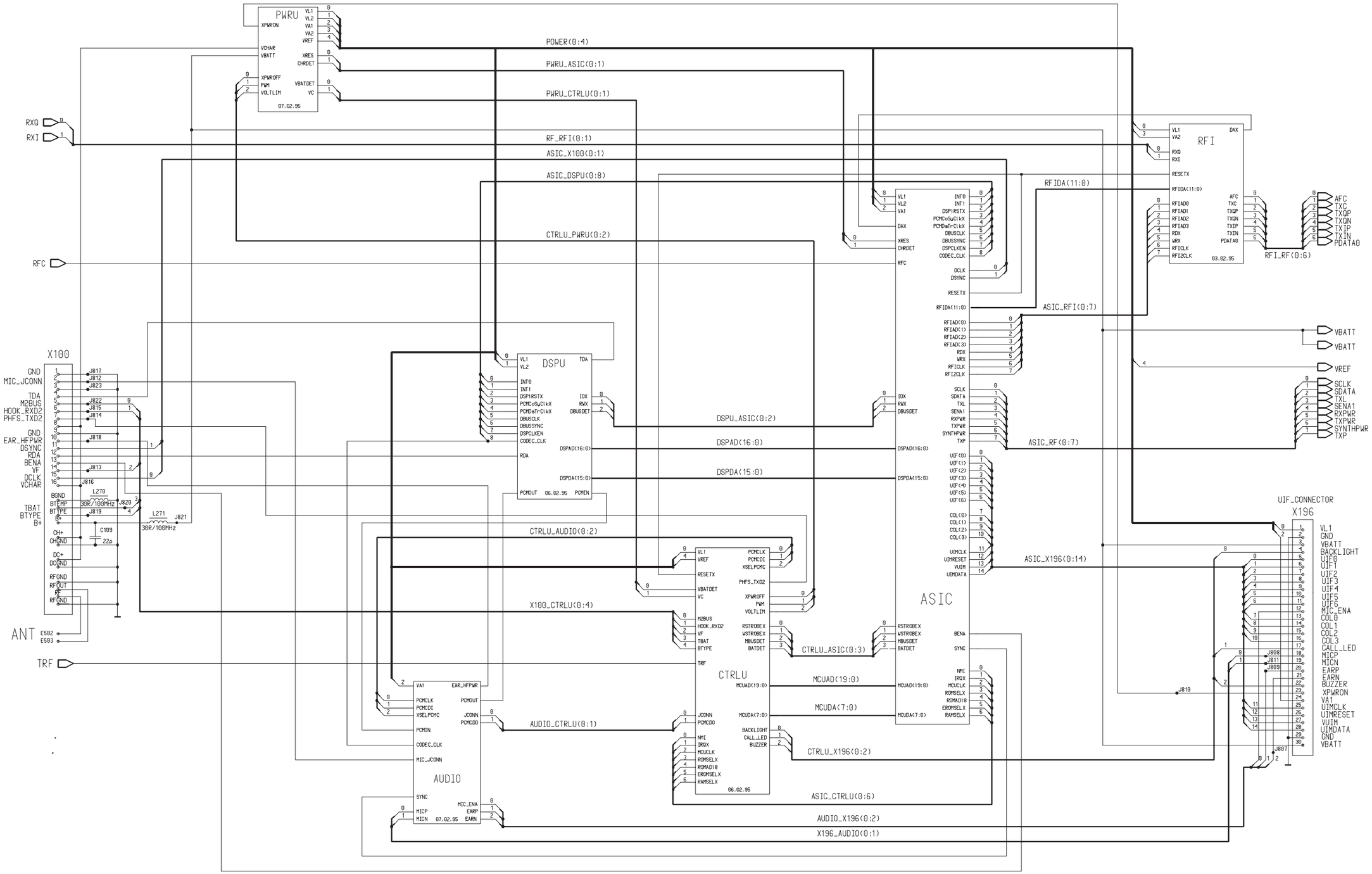
Block Diagram of System Section (Version 9.0)



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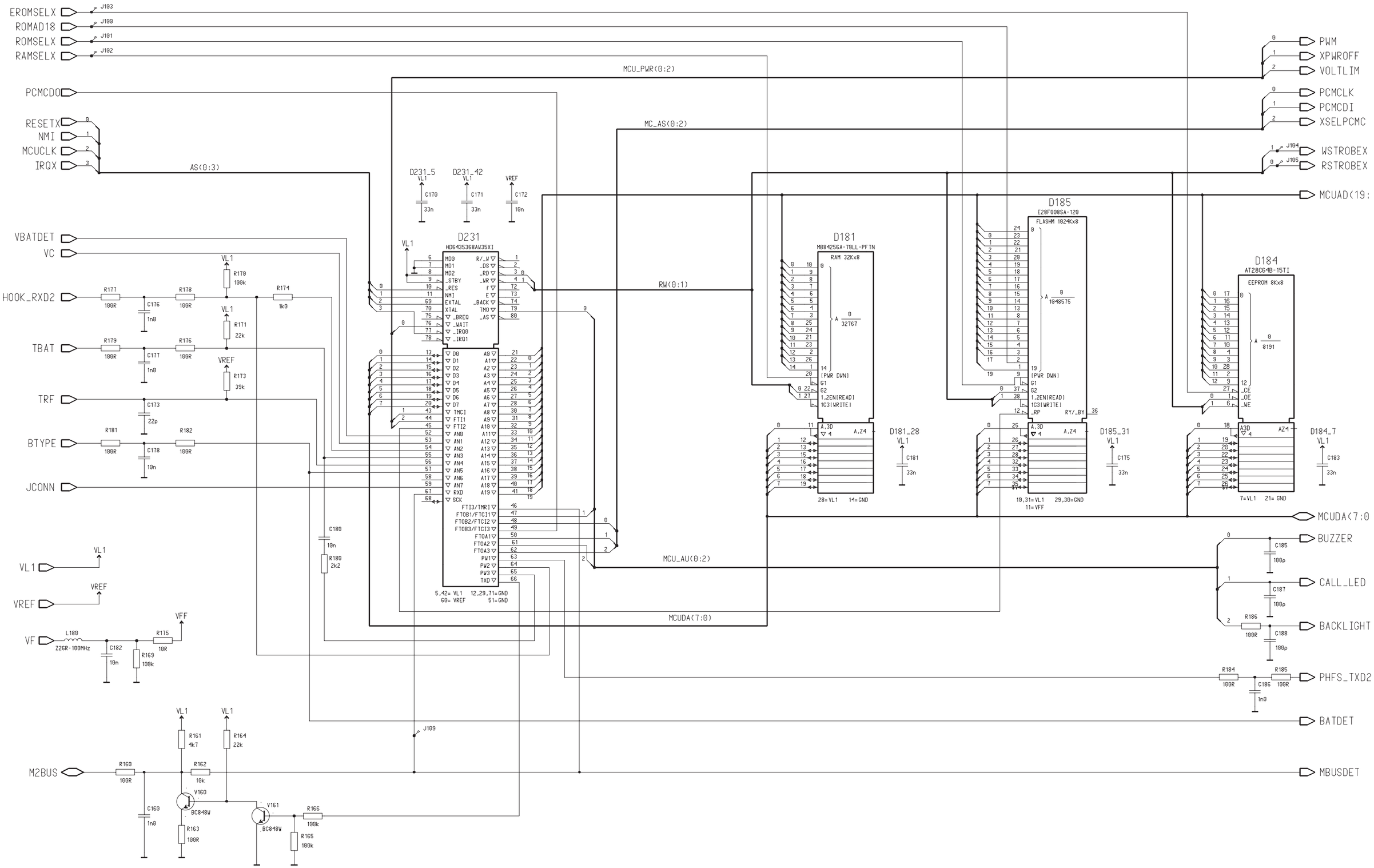
Circuit Diagram of CTRLU Section (Version 9.0)



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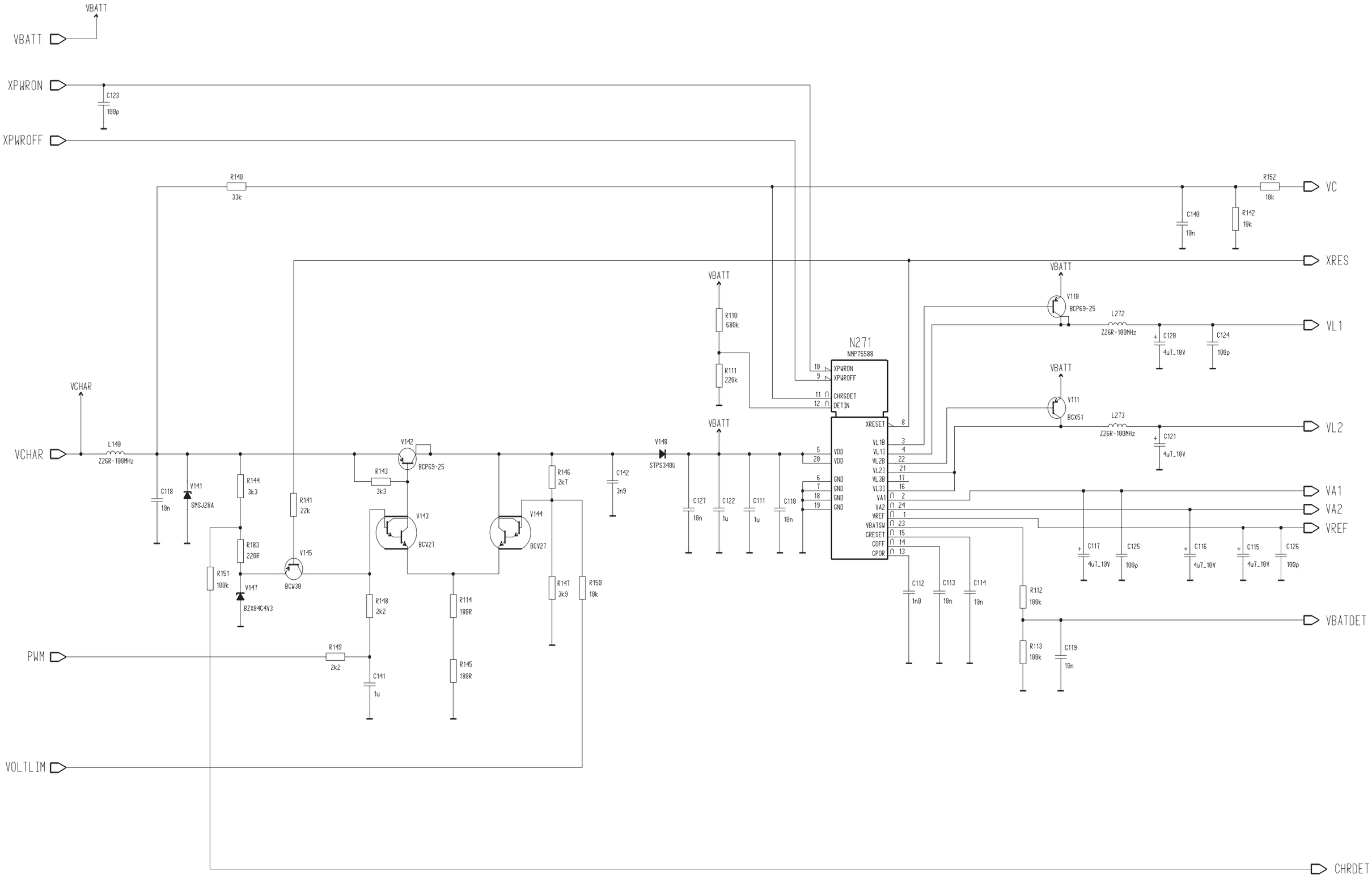
Circuit Diagram of PWRU Section (Version 9.0)



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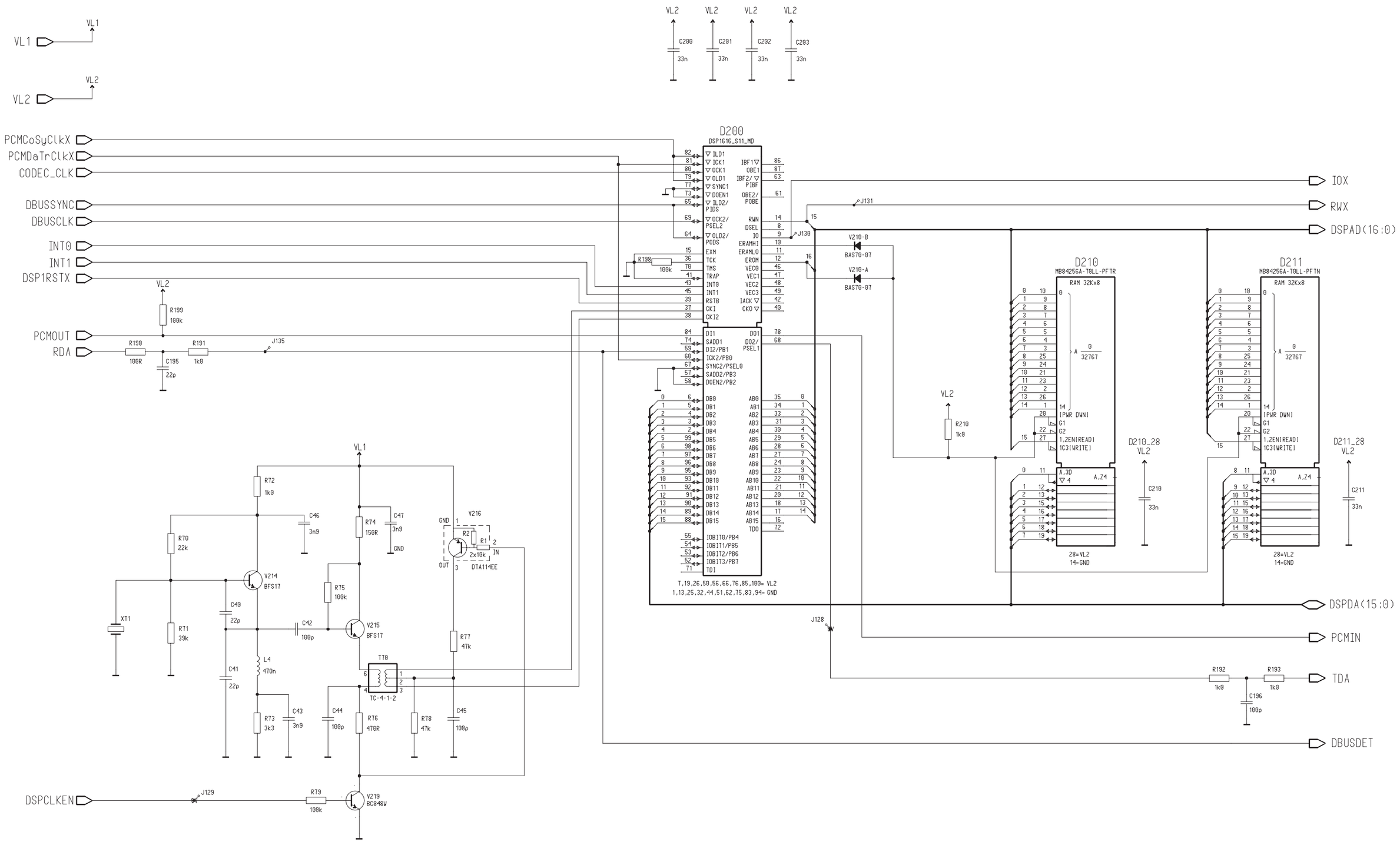
Circuit Diagram of DSPU Section (Version 9.0)



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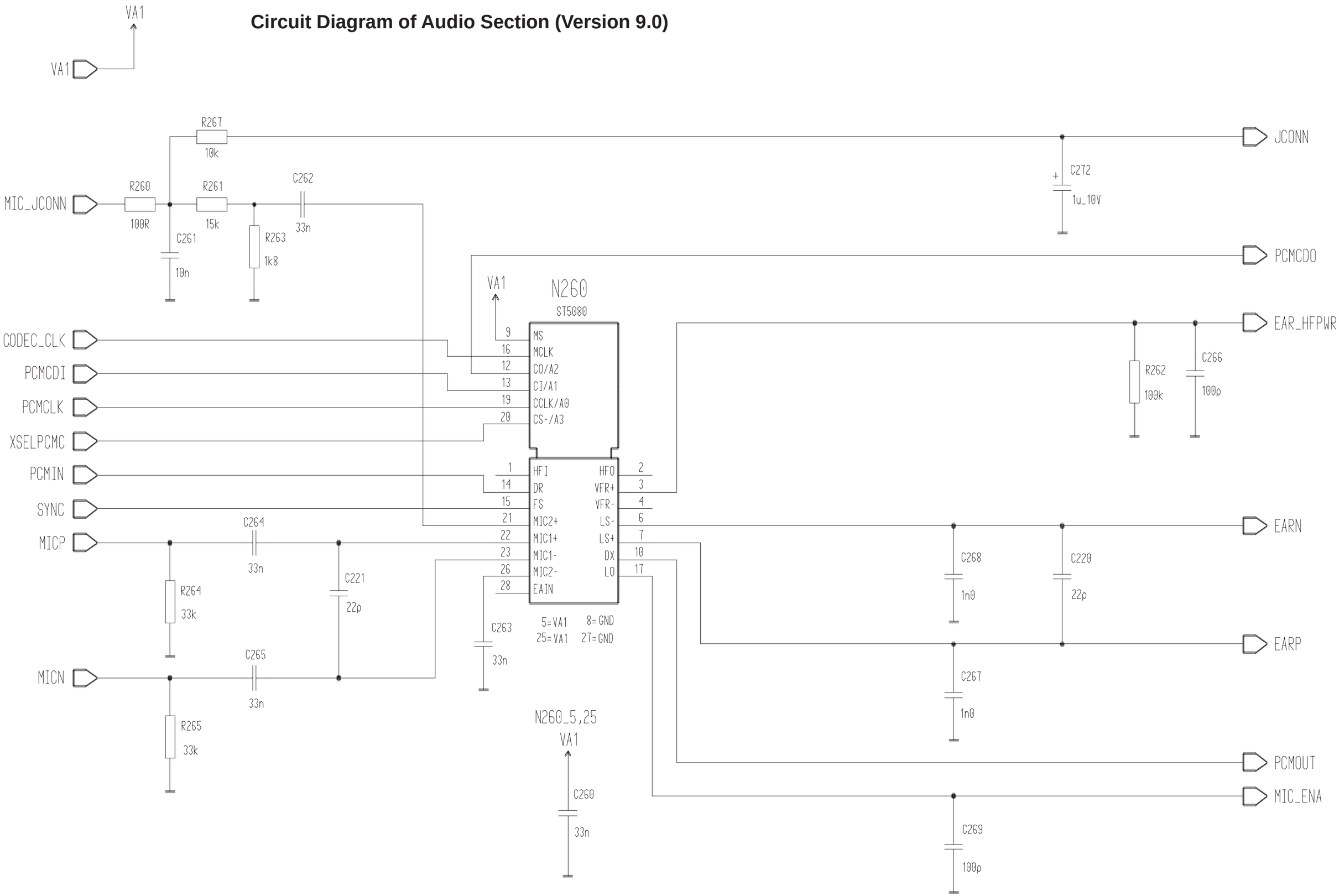
Circuit Diagram of Audio Section (Version 9.0)



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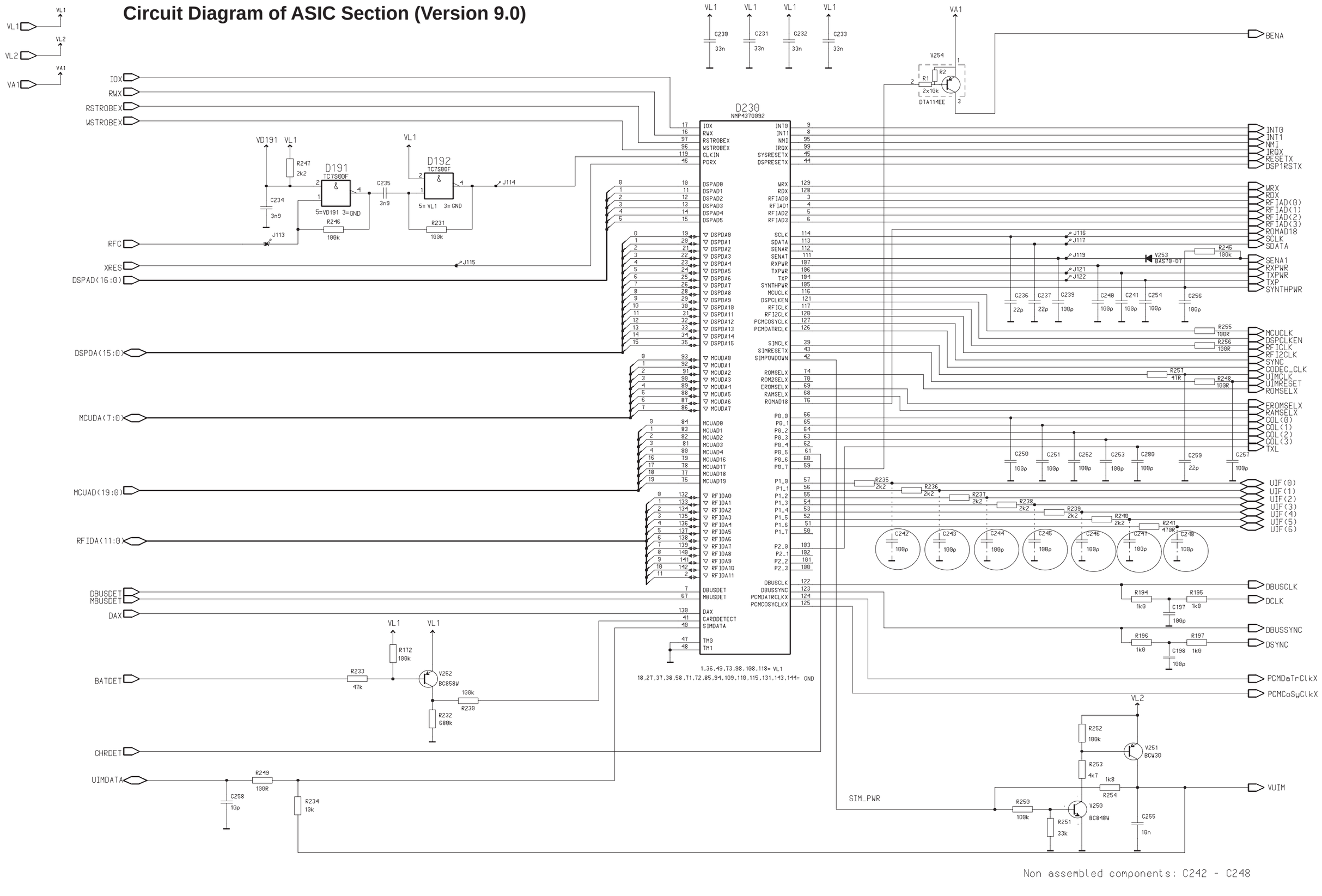
Circuit Diagram of ASIC Section (Version 9.0)



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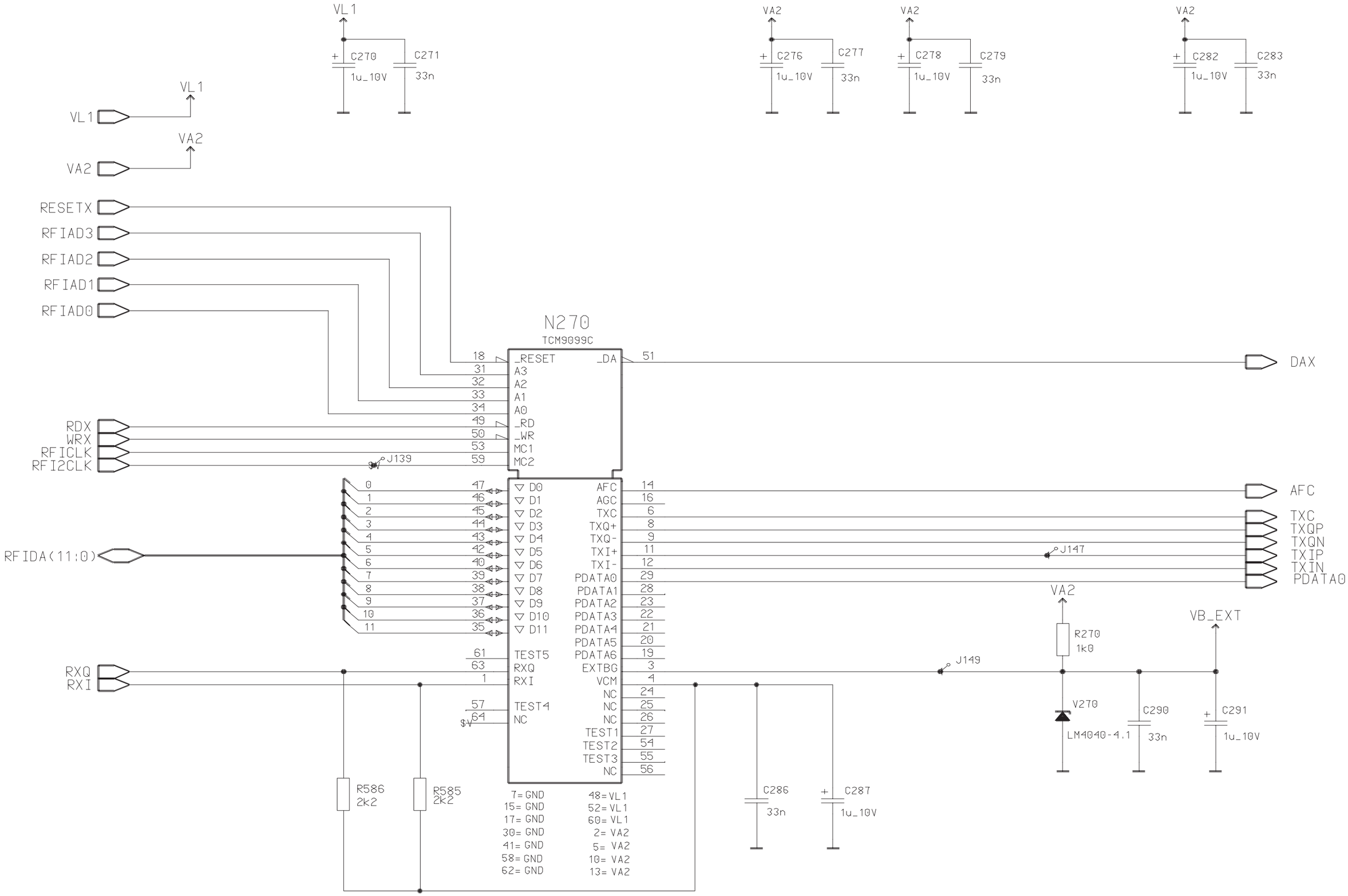
Circuit Diagram of RFI Section (Version9.0)



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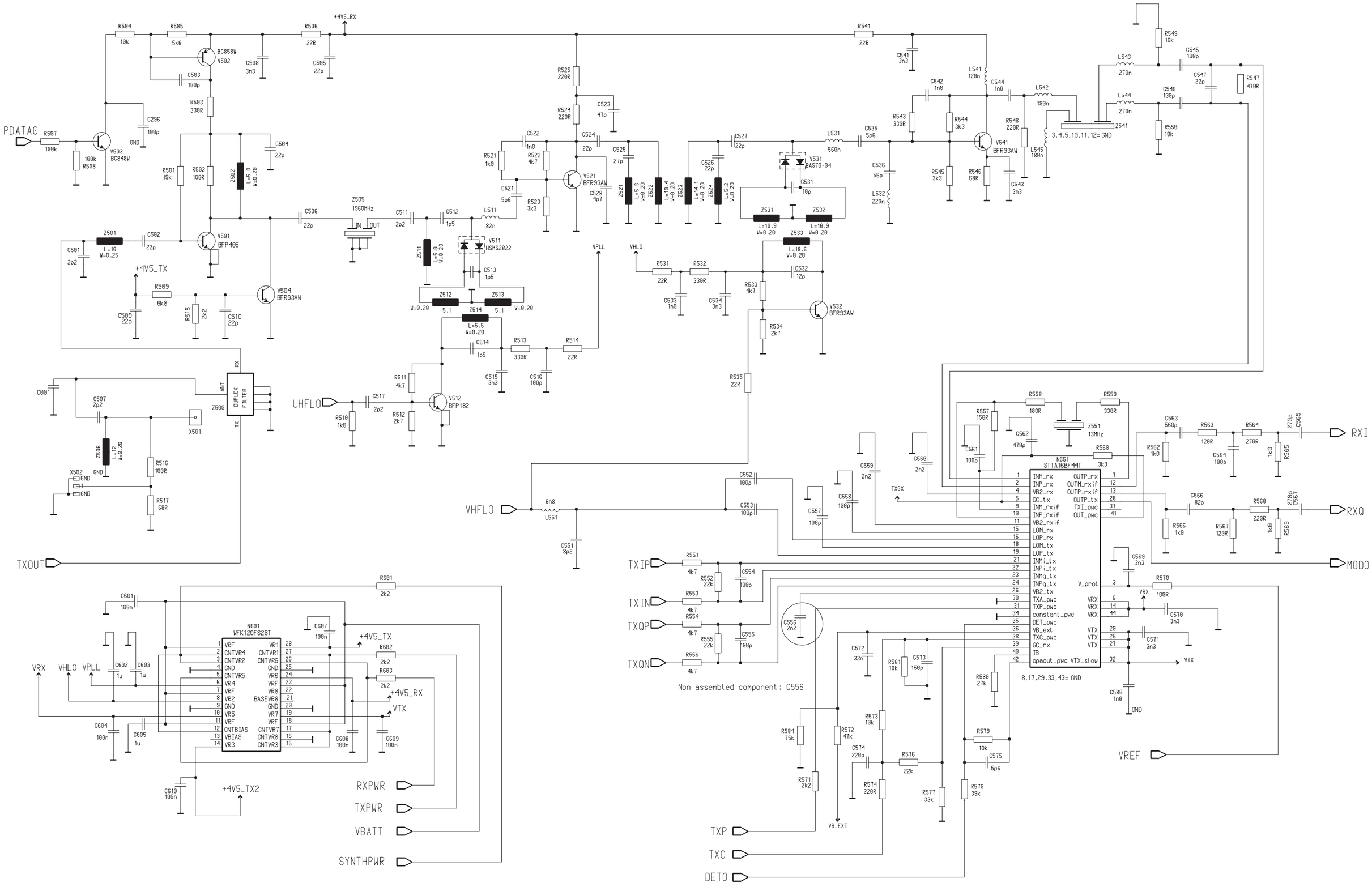
Circuit Diagram of Receiver Section (Version 9.0)



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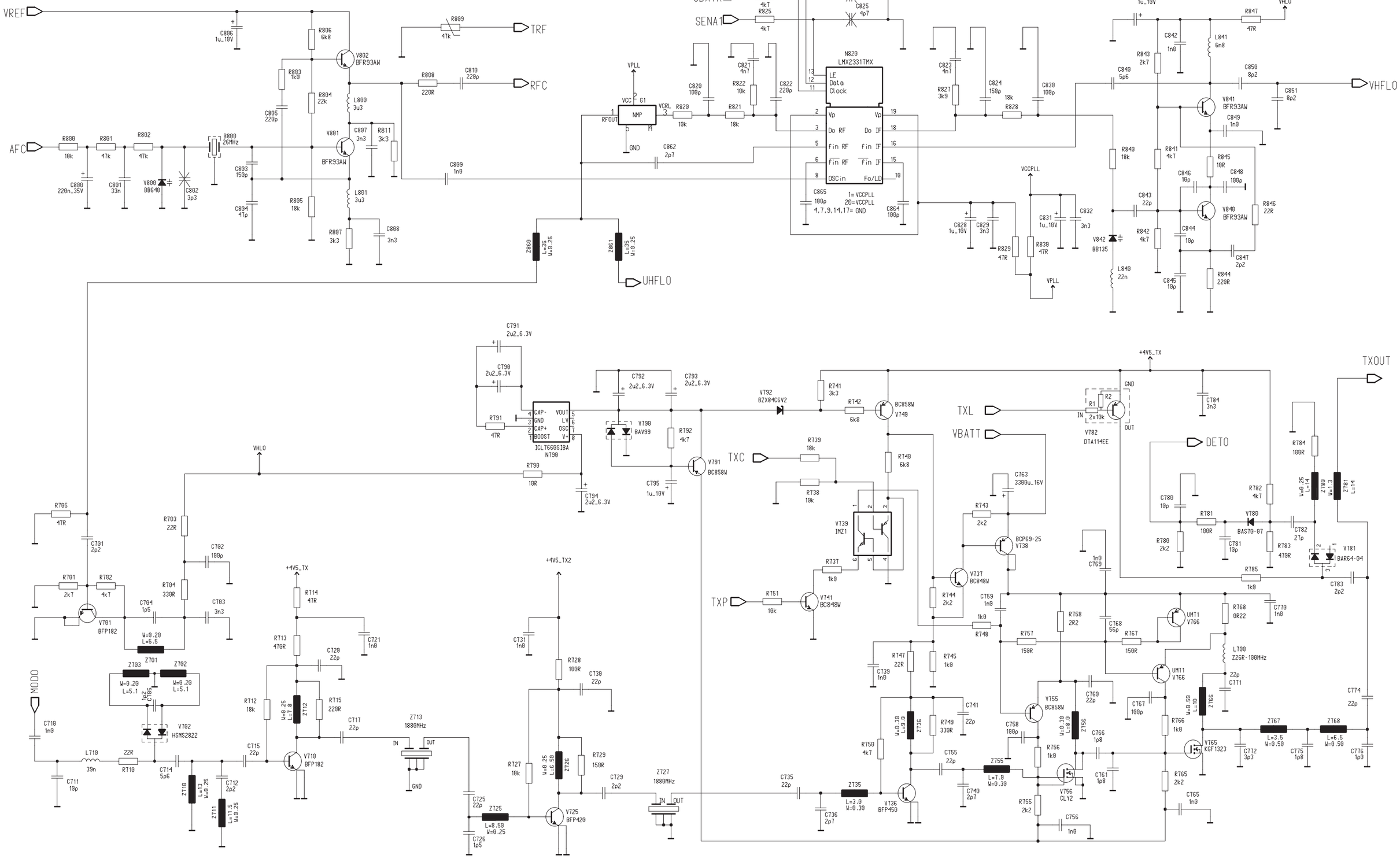
Non assembled components: C802, C825, C826, C827

Circuit Diagram of Transmitter Section (Version 9.0)



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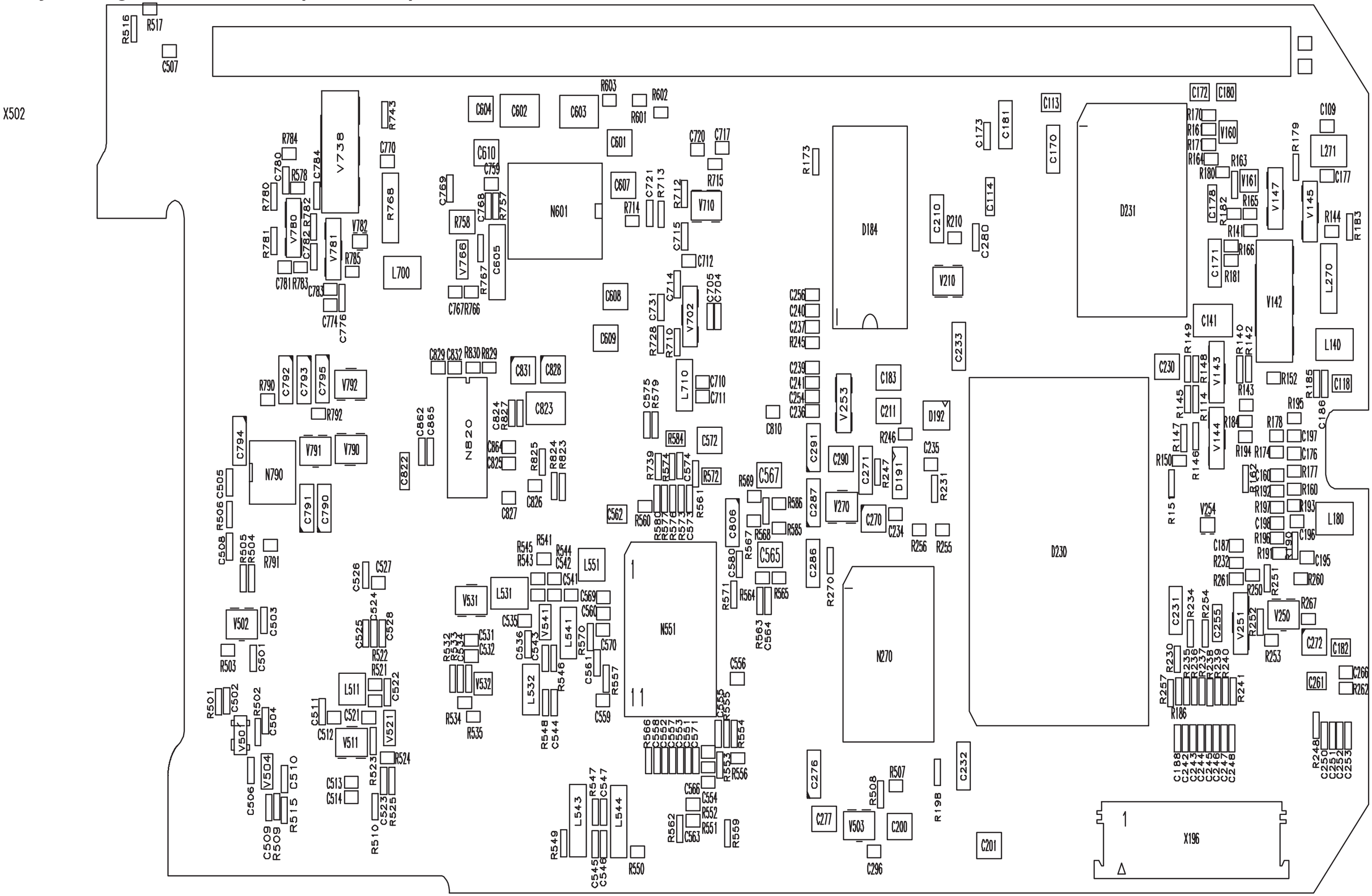
Layout Diagram of DB1 side 1 (Version 9.0)



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Layout diagram of DB1 side 2 (Version 9.0)